

Low-Cost RFID IC Packaging and Assembly: Roadblock on the Highway to a 5-cent RFID Tag

A Private Workshop hosted by TechSearch International, Inc.

March 29-30, 2005
3500 W. Balcones Center Drive
Austin, Texas

March 29, 2005	March 30, 2005
8:30-9:00 Registration and coffee	8:30-9:00 Coffee
9:00-9:15 Introduction and Greetings Jan Vardaman, President, TechSearch International	9:00-9:30 RFID Tag Activity at TI Enu Waktola, Texas Instruments
9:15-9:45 RFID Scorecard: Accomplishments and Issues Rick Koskella, Sun Microsystems	9:30-10:00 Advanced Tag Assembly Gerald Steinwasser, Muhlbauer
9:45-10:30 RFID Activity at Philips Dr. N.J.A. van Veen, Philips	10:00-10:15 Morning Break
10:30-10:45 Morning Break	10:15-10:45 Automated RFID Tag Assembly: A Turnkey Line Yoshio Nogami, Toray Engineering
10:45-11:30 A Cost Model for RFID Dr. Gitanjali Swamy, Harvard Business School	10:45-11:15 Printed Electronics: A Future Solution? Dr. Dan Gamota, Motorola
11:30-12:00 Fluidic Self Assembly Glenn Gengel, Alien Technology	11:15-12:00 RFID in Japan Tetsuya Onishi, Grand Joint Technology
12:00-1:00 Lunch	12:00-1:00 Lunch
1:00-1:45 RFID Chip and Tag Assembly Eitan Avni, KSW Microtec	
1:45-2:15 RFID Inlay Assembly at Symbol Technologies Joseph White, Symbol Technologies	
2:15-2:45 Break	
2:45-3:15 A Novel Approach to Low-Cost RFID Inlay Assembly Alan Isaacson and Donald Black, Celis Semiconductor	
3:15-3:45 RFID Tag Assembly Activities in China TBD	
3:45-5:00 Panel Discussion: The Key Challenges to Lowering RFID Inlay and Tag Assembly Cost Moderator - Dr. Frank J. Bachner, Senior Analyst, TechSearch International	
6:30- Dinner Iron Works Barbeque	



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